

# GOVT. COLLEGE OF ENGINEERING, KARAD

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No. : GCOEK/E&TC/2022/ 958 A

Date: 8 MAR 2022

To,

Notice Board

## QUOTATION

Sub : Quotation for following software. ( Due Date 21.03.2022)

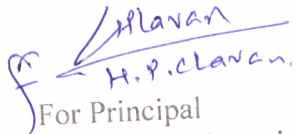
**Sealed Quotations** are invited from eligible and interested manufacturers/ dealers/distributors/ for the following items on the terms and conditions mentioned below.

Sr. No	Particulars	Qty	Rate Per	GST %	Total Cost
1	<p><b>Proteus VSM Software</b></p> <p><b>Proteus VSM - Product Specifications-</b> Proteus VSM brings Agile development into the embedded workflow. It enables rapid prototyping of both hardware design and firmware design in software, making it easy to make changes to both. Design, Test and Debug your embedded projects before a physical prototype is ordered. circuit simulation, animated components and microprocessor models to facilitate co-simulation of complete microcontroller based designs. For the first time ever, it is possible to develop and test such designs before a physical prototype is constructed. This is possible because you can interact with the design using on screen indicators such as LED and LCD displays and actuators such as switches and buttons. The simulation takes place in real time (or near enough to it): a 300 MHz Pentium II can simulate a basic 8051 system clocking at over 12MHz. Proteus VSM also provides extensive debugging facilities including breakpoints, single stepping and variable display for both assembly code and high level language source. The VSM architecture allows additional animated models to be created by anyone, including end users. Whilst many types of animated model can be produced without resort to coding, a documented interface has been provided for developers to write their own models packaged as Windows DLLs. These models can implement purely electrical behaviour or combine this with graphical behaviour so that almost any kind of application specific peripheral can be simulated.</p> <p><b>Schematic Entry</b> Proteus VSM uses our proven Schematic Capture software to provide the environment for design entry and development. ISIS is a long established product and combines ease of use with powerful editing tools. It is capable of supporting schematic capture for both simulation and PCB design. Designs entered in to Proteus VSM for testing can be netlisted for PCB layout either with our own PCB Design products or with third party PCB layout tools. ISIS also provides a very high degree of control over the drawing appearance, in terms of line widths, fill styles, fonts, etc. These capabilities are</p>	05 Use rs			

<p>used to the full in providing the graphics necessary for circuit animation.</p> <p><b>Circuit Simulation</b> At the heart of Proteus VSM is ProSPICE. This is an established product that combines uses a SPICE3f5 analogue simulator kernel with a fast event-driven digital simulator to provide seamless mixed-mode simulation. The use of a SPICE kernel lets you utilise any of the numerous manufacturer-supplied SPICE models now available and around 6000 of these are included with the package.</p> <p><b>Co-Simulation of Microcontroller Software</b> The most exciting and important feature of Proteus VSM is its ability to simulate the interaction between software running on a microcontroller and any analog or digital electronics connected to it.</p> <p><b>Source Level Debugging</b> Whilst Proteus VSM is already unique in its capability to run near real time simulations of complete micro-controller systems, its real power comes from its ability to perform these simulations in single step mode. This works just like your favourite software debugger, except that as you single step the code, you can observe the effect on the entire design - including all the electronics external to the microcontroller.</p> <p><b>Model Library</b> In addition to the microprocessor models for each supported family, each Proteus VSM package includes a library of over 6000 device models.</p>				
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#### Terms & Condition

10. The quotation should be sent to "The Principal, Government College of Engineering, Karad" on or before 21.03.2022 at 5.30PM.
11. Quotations will be opened at 3.00 PM on 22.03.2022.
12. Mention make / model no., warranty & delivery period of equipment quoted.
13. Quotation validity for at least 45 days from the date of opening.
14. Delivery at F.O.R. Govt. College of Engineering Karad.
15. 100% payment after satisfactory delivery & installation.
16. No extra charges will be paid for cartage etc. the material rejected and replaced.
17. After delivery quality conformance undertaking of equipment/machinery
18. The undersigned reserves the right to accept or reject any offer or all offers without assigning any reason thereof.

  
 H.P. Clavan.  
 For Principal  
 S. R. Kulkarni, HOD- E&TC Engineering